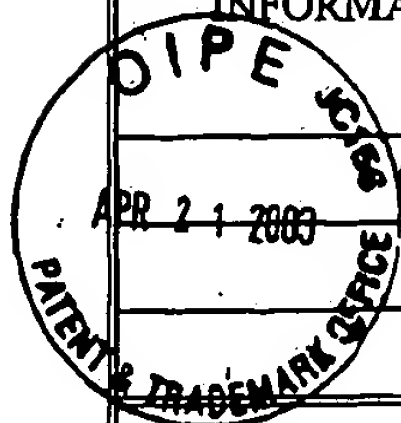
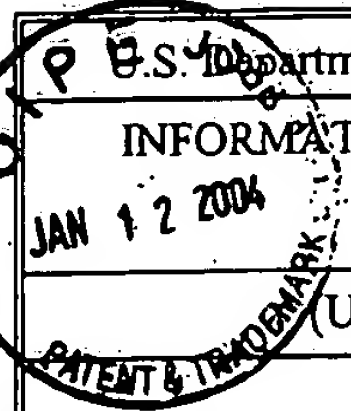


U.S. Department of Commerce, Patent and Trademark				Atty Docket No.		Application No.		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				SNDK.111US1 (formerly M-10237-1P US)		09/925,134		
				Applicants				Eliyahou Harari et al.
				Filing Date		Group		
				August 8, 2001		2814		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
<i>K2</i>		5,949,101	Sep. 7, 1999	Aritome				
<i>K2</i>		6,037,221	Mar. 14, 2000	Lee et al.				
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Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
<i>K2</i>		WO 97/32309	4 Sep. 1997	WIPO			X	
<i>K2</i>		0 601 747 A2	15 Jun. 1994	EPO			X	
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
<i>K2</i>		"Notification of Transmittal of the International Search Report or the Declaration", European Patent Office, International Searching Authority, January 29, 2003, 7 pages.						
Examiner <i>Kuh Q. hvt</i>		Date Considered <i>9/10/03, 4/11/04</i>						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								



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	SN DK.111US1	09/925,134
	Applicants	Conf. No.
	Harari et al.	6957
	Filing Date	Group
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<i>KL</i>	1	4,112,507	9/5/78	White et al.			
	2	5,070,032	12/3/91	Yuan et al.			
	3	5,095,344	3/10/92	Harari			
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<i>KL</i>	11	2002/0130350	9/19/02	Shin et al.			

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<i>KL</i>	12	Chan et al., "A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device," <i>IEEE Electron Device Letters</i> , Vol. EDL-8, No. 3, March 1987, pp. 93-95.
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U.S. Department of Commerce, Patent and Trademark	Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	SNDK.111US1	09/925,134
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<i>RL</i>	15	5,712,180	1/27/98	Guterman et al.			
	16	5,851,881	12/22/98	Lin et al.			
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<i>RL</i>	27	DiMaria et al., "Electrically-Alterable Read-Only-Memory Using Si-rich SiO ₂ Injectors and a Floating Polycrystalline Silicon Storage Layer," <i>J. Appl. Phys.</i> 52(7), July 1981, pp. 4825-4842.
<i>RL</i>	28	Hori et al., "A MOSFET with Si-Implanted Gate-SiO ₂ Insulator for NonVolatile Memory Applications," <i>IEDM</i> 92, April 1992, pp. 469-472.

Examiner *Kenn Quinto* Date Considered *4/2/04*

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